

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	238	using adj5 "and" adj5 "or" adj4 gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 07:36
S1	101	(convert\$3 or transform\$3 or translat\$3) same (planar adj3 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 07:35
S2	2	(convert\$3 or transform\$3 or translat\$3) same (planar adj3 transistor) same (double adj2 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:19
S3	26	(planar adj3 transistor) same (double adj2 transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:29
S4	4	(planar adj3 transistor) same (double-gate adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:26
S5	77	(double-gate adj transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:26
S6	1	S1 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:26
S7	6791	double and gate adj transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:27

S8	210	double adj gate adj transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:27
S9	1	S1 and S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:27
S10	7	fin and S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/03 14:29
S11	2213	"and" same "xOR" same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:55
S12	3	"and" same "xOR" same operation same cad	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:54
S13	983	"and" same overlap\$4 same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:55
S14	26	("and" adj3 function) same overlap\$4 same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 09:56
S15	3	("and" adj3 function) same overlap\$4 same pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:28

S16	53638	overlap\$4 same pattern	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:28
S17	860	overlap\$4 same pattern same layout	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:29
S18	2	overlap\$4 same pattern same layout same "and" same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:31
S19	0	(non adj overlap\$4) same pattern same layout same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:32
S20	6	"xor" same pattern same layout same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:35
S21	59	"xor" same layout same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:56
S22	132	(active adj layer) same (gate adj layer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:57
S23	24	(active adj layer) same (gate adj layer) same over!\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/09 10:57

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Electronics Letters , Volume: 26 , Issue: 19 , 13 Sept. 1990

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VLSI Design, 1997. Proceedings., Tenth International Conference on , 1997

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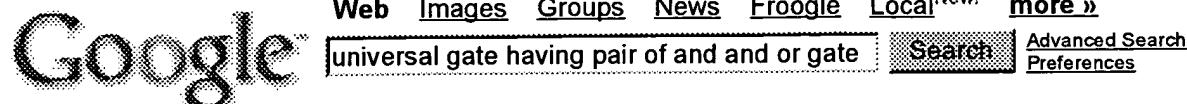
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... 2 . Let {**H** 1 ,**H** 2 ,**H** 3 } be a **universal** set of ... Suppose we are given the **gate** **G** above as a black box and ... that **f** is either the constant function **f** 1 (**having** **f** 1 ...

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